

FIG. 1

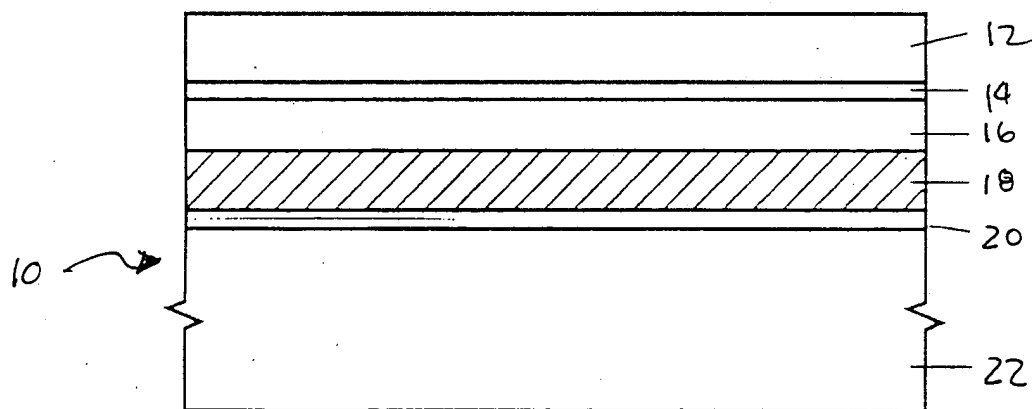


FIG. 2

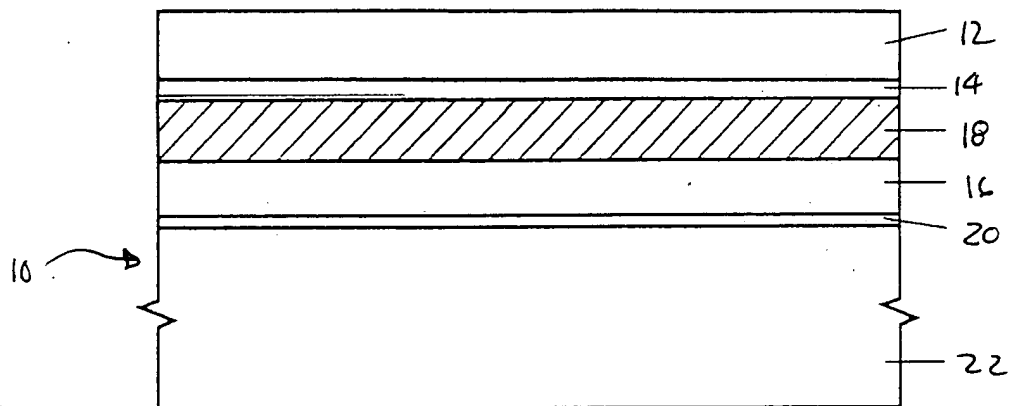


FIG. 3

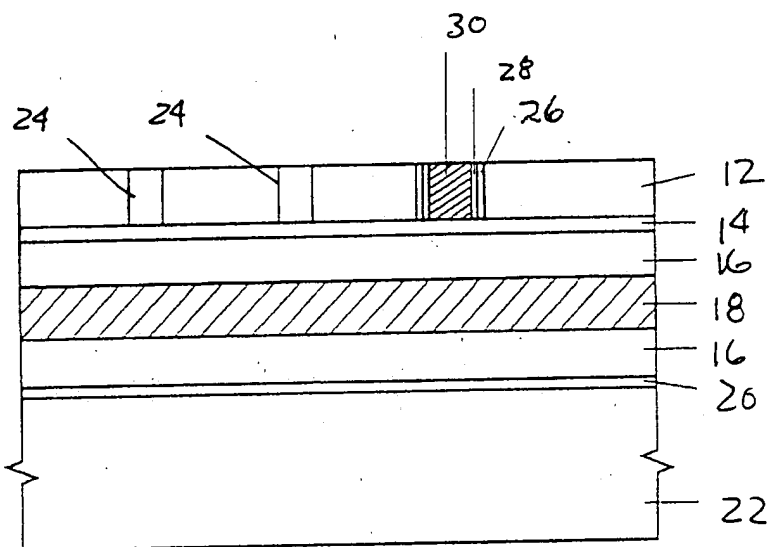


FIG. 4

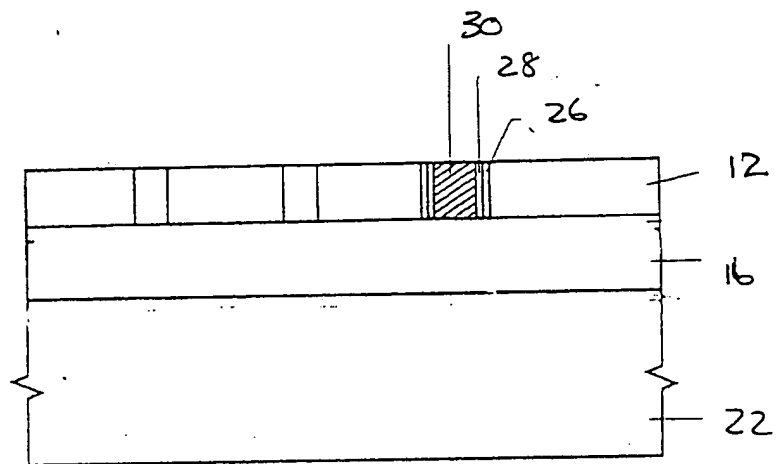


Fig. 5

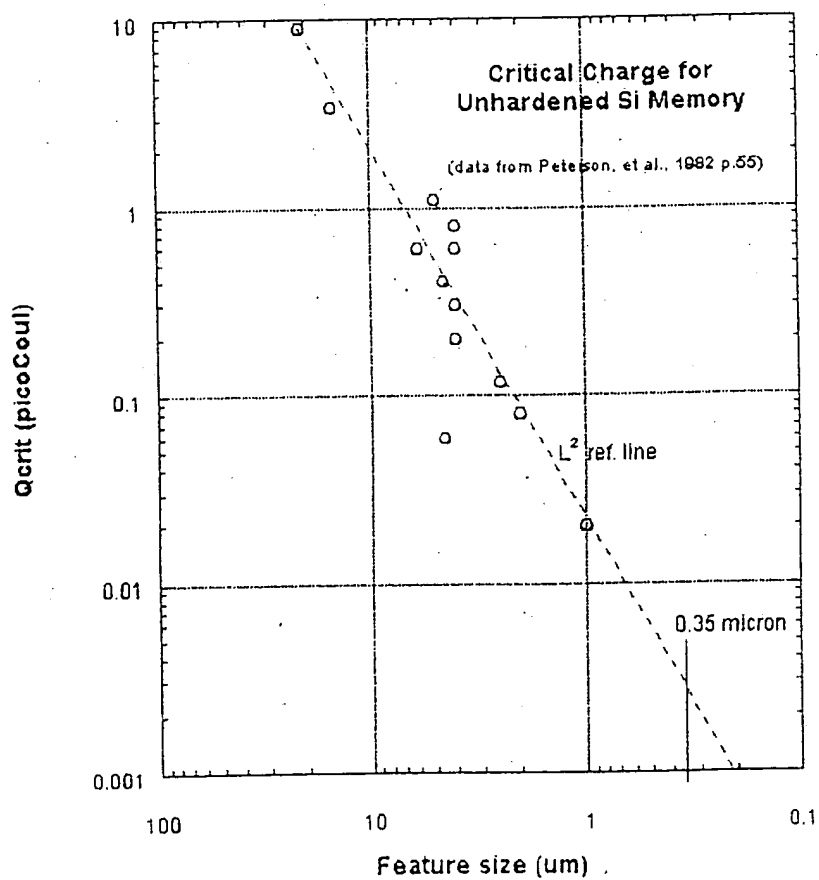


Fig. 6

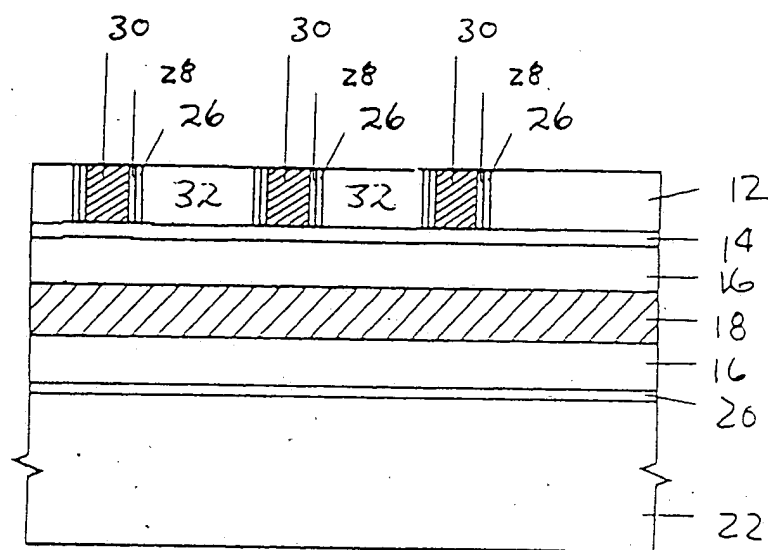


FIG. 7

## LET on Silicon

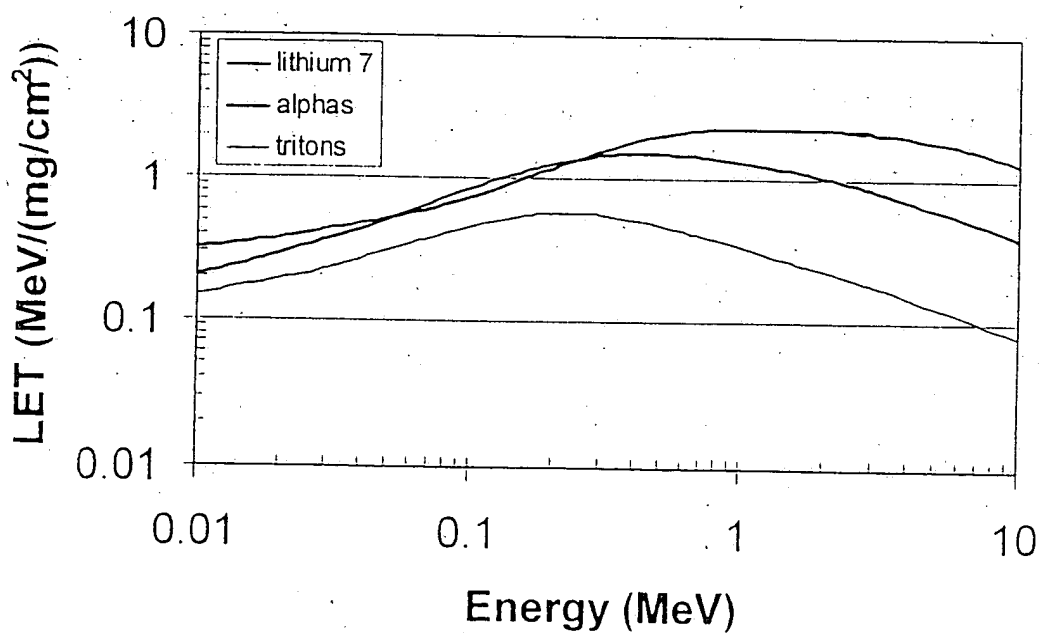


FIG. 8

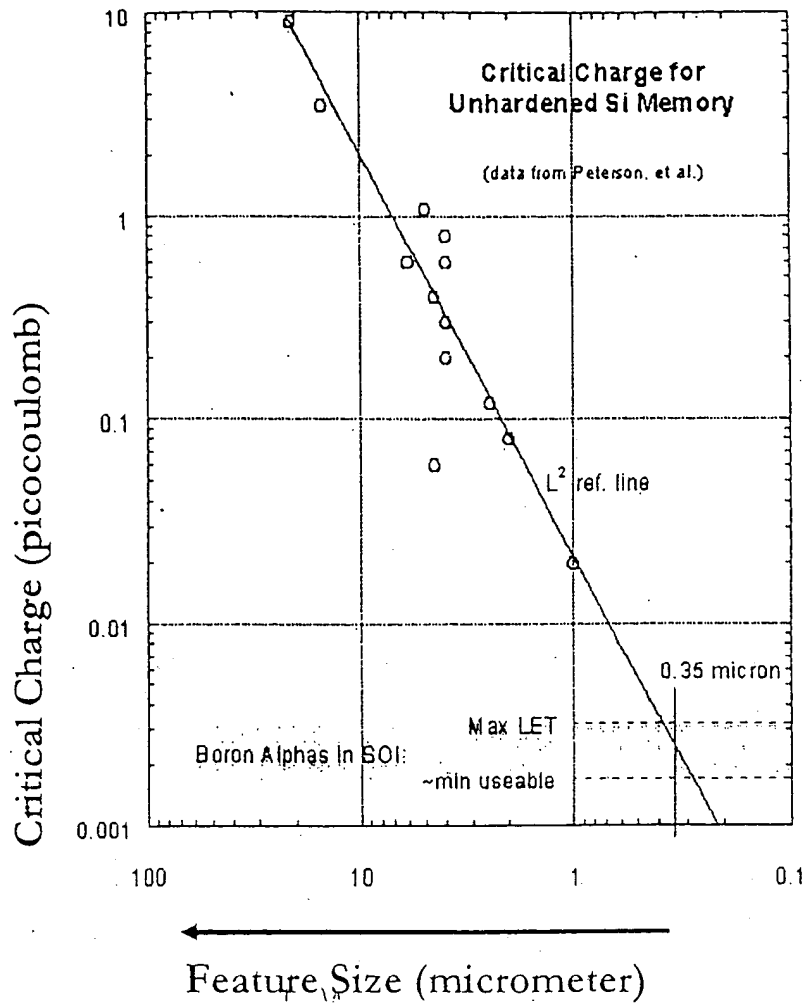


FIG. 9